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October 22, 2002 Srilakshmi, Kumar TO DATE NAME ATTORNEYS AT LAW USPTO 703-746-9540 COMPANY/FIRM FAX# FOURTH FLOOR 1755 JEFFERSON DAVIS HIGHWAY ARLINGTON, VIRGINIA 22202 CONFIRM FAX: YES NO NUMBER OF PAGES INCLUDING COVER: Satheesh Karra (703) 413-3000 **FROM** (703) 413-2220 FACSIMILE OUR REFERENCE NAME QBLONPAT@OBLON.COM YOUR REFERENCE DIRECT PHONE #

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Proposed Draft for 09/512,817

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Dear Examiner Kumar,

Enclosed is a proposed amendment to independent claims 1, 8, 16, and 17. During 10/23 meeting with you, Applicants would like to discuss rejection of independent claims, and distinct dependent claims from each of the claim sets, such as, for example, claims 2-7, and 11 over U.S. Patent No. 5,852,429 to Scheffer et al. Thank you,

Regards, Satheesh Karra 703 413 6509

PROPOSED DRAFT for 09/512,817

1. (Amended) In a driving method for a liquid crystal display device, the method comprising:

selecting simultaneously a plurality of lines of row electrode in a liquid crystal display device comprising a plurality of row electrodes and a plurality of column electrodes; and

applying predetermined voltages to the selected lines of <u>the</u> row electrode during a selection period, [the driving method being characterized in that:] wherein

the selection period of a display frame is divided, and column electrodes are driven with a voltage pattern so as to reduce a change of voltage level in each of the divided periods.

PROPOSED DRAFT for 09/512,817

Page 2 of 3

8. (Amended) In a driving method for a display device having display elements in a matrix form and producing voltage levels for effecting gradation display, the method [for a display device being characterized in that]comprising:

[in a plurality of continuous display frames,] setting a time of at least one frame period [is made]to be different from that of another frame period, in a plurality of continuous display frames;[,]

dividing the selection period of at least one frame in the plurality of display frames [is divided]into divided selection periods[,]; and

providing on-data and off-data [are provided]in the selection period of the non-divided frame period and the divided selection periods to produce a plurality of voltage levels[, and]; wherein

the plurality of voltage levels are used for a display except for the voltage levels in the vicinity of [the]highest [level] and [the]lowest voltage levels.

16. (Amended) In a driving device for a liquid crystal display device for selecting simultaneously a plurality of lines of row electrode in a liquid crystal display device comprising a plurality of row electrodes and a plurality of column electrodes, and applying predetermined voltages to the selected row electrodes during a selection period, the driving device [being characterized by]comprising a driving means for driving column electrodes according to a predetermined voltage

PROPOSED DRAFT for 09/512,817

Page 3 of 3 pattern in each period formed by dividing a selection period of a display frame so that the divided selection periods have a different time ratio.

17. (Amended) In a driving device for a liquid crystal display device for selecting simultaneously a plurality of lines of row electrode in a liquid crystal display device comprising a plurality of row electrodes and a plurality of column electrodes and applying predetermined voltages to the selected row electrodes during a selection period, the driving device[being characterized by comprising]including a driving means, the driving device further comprises:

a timing control means which forms a combination of at least one of two continuous display frames in which time ratio of a display frame period to the other is within 50 – 90%, and supplies a timing signal to column drivers for driving column electrodes, [a timing signal] so that a selection period of at least one of the two continuous display frames is divided into two portions to produce an n (n: an integer of at least 3) number of divided periods,

a gradation processing means for producing n-bit gradation data based on inputted image data to write the n-bit gradation data in frame memories, and

a column data producing means for producing column data by reading sequentially the n-bit gradation data which are stored in the frame memories in the respective divided periods and supplying the produced data to the column drivers.